This document regroups all links necessary for the user to develop an application

ECPA-IMB-24-0050-2
Mar. 12, 2024

RZ/V2H Easy Download Guide

Step	Launch of board and evaluation environment	Contents Type	Contents Name	RZ/V2H Link
1	User's Manual			
		Document	User's Manual: Hardware	Link
		Document	[NDA]User's Manual: Hardware (Additional document)	Link
2	How to start up the board			
		Document	RZ/V2H Evaluation Board Kit User's Manual	Link
		Board	RZ/V2H Evaluation Board Kit	Link
3	Evaluation environment start-up (AI SDK)			
		Information	Renesas AI SDK WEB Page	Link
		Document	AI SDK v3.00 Release Note	Link
		Compressed file	AI SDK (RTK0EF0180F03000SJ.zip)	Link
		|--- Document	|--- Release Note	
		|--- Software	|--- Pre-Build for eSD(Bootloader, Linux Kernel image, Device Tree, Filesystem)	
		|--- Software	|--- Application cross compiler installer	
		|--- Software	|--- Dockerfile (Dockerfile for installing AI SDK)	
		|--- Tool	|--- DRP-AI Translator i8 installer	
		|--- Document	|--- DRP-AI Translator User's Manual	
		|--- Document	|--- DRP-AI Quantizer User's Manual	
		|--- Document	|--- Component List	
		|--- Document	|--- Linux License List	
		Compressed file	AI SDK Sauce code (RTK0EF0180F03000SJ_linux-src.zip)	Link
		|--- Software	|--- Yocto recipe package	
		|--- Document	|--- Readme	
4	How to launch various tools			
		Tool	DRP-AI TVM (DRP-AI Translator i8)	Link
		Tool	DRP-AI Extension Pack (Pruning Tool) Version 1.0.0	Link
		Document	DRP-AI Extension Pack (Pruning Tool) User's Manual	Link
		Tool	e² studio 2024-01.1 installer for Windows	Link
		Tool	e² studio 2024-01.1 Linux Host	Link
		Document	Release Note for e² studio	Link
		Tool	Smart Configurator	Link
		Document	Smart Configurator for RZ User's Guide	Link

Step	Custom board development	Contents Type	Contents Name	RZ/V2H Link
	LPDDR4/4X Controller Configuration	Compressed file	[NDA]DDRTOP Application Note	Link
		|--- Document	Application Note	
		|--- Document	LPDDR4/4X Controller Configuration Guide	
		|--- Tool	Setting tool	
	PCB Design Guide	Document	[NDA]PCB Design Guidelines	Link
	Thermal Design Guide	Document	Thermal Design Guide	Link
	Lifetime estimation guide	Document	[NDA]Lifetime Guideline	Link
	LSI Design Model (IBIS)	Model	[NDA]LSI IBIS	Link
	Interface Design Model (Spara, IBIS)	Model	[NDA]Interface (Spara, IBIS)	Link
	Reference Desing Data	Compressed file	CPU Board Design Data	Link
		|--- Desing Data	|--- Schamtics	
		|--- Desing Data	|--- BOM List	
		|--- Desing Data	|--- Circuit Data (.DSN)	
		|--- Desing Data	|--- Layout Data (.brd)	
		|--- Desing Data	|--- Gerber Data	
	Reference Desing Data	Compressed file	EXP Board Design Data	Link
		|--- Desing Data	|--- Schamtics	
		|--- Desing Data	|--- BOM List	
		|--- Desing Data	|--- Circuit Data (.DSN)	
		|--- Desing Data	|--- Layout Data (.brd)	
		|--- Desing Data	|--- Gerber Data	

Step	Information for engineers	Contents Type	Contents Name	RZ/V2H Link
	RZ/V Series 64-bit MPU (**Renesas info**)	Link summary	Introduction,Boards,Software and Documentation	Link
	FAQ	Link summary	Knowledgebase / FAQ	Link
	Contact Support	QA Ticket	Technical Q&A	Link